IN THE SPECIFICATION

Please amend the paragraph appearing at page 49, line 11 – page 50, line 5 as follows:

--Fig.14 shows a modification of the copying management information. Of the eight bits b7 to b0, upper two bits of b7 and b6 are allocated as the information bits CM_C of the copying generation management system (CGMS) for limiting the copying generation, the next two bits b5 and b4 are allocated as trigger bits CM_T of the analog protection system (APS) and the next bit b3 is allocated as the bit CM_A specifying whether or not the source is an analog source. The remaining two bits b2 to b0 are undefined. If the bits (b7, b6) of the information bits CM_C ~~APS trigger bits CM_T~~ are (0, 0), (1, 0), (1, 1) or (0, 1), these specify copy free, one generation copying possible, copy not permitted and unused, respectively. If the bits (b5, b4) of the APS trigger bits CM_T are (0, 0), (0, 1), (1, 0) or (1, 1), these specify off state, turning the pseudo-sync pulse and a split burst or color stripe as later explained on and off, respectively, turning the PSP and the 2-line split burst on, and turning the PSP and the 4-line split burst on, respectively. If the bit b3 of CM_A is 1 or 0, it specifies an analog package medium and otherwise, respectively.--